

JPV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Edward R. Rhoads et al.	§	Art Unit: 2167
	§	
Serial No.: 10/803,320	§	Examiner: Susan F. Rayyan
	§	
Filed: March 18, 2004	§	Atty Docket: ITL.0308C1US
	§	P7989C
For: Accessing File Data Stored in	§	
Non-Volatile Re-Programmable	§	Assignee: Intel Corporation
Semiconductor Memories	§	

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO NOTICE OF NON-RESPONSIVE AMENDMENT

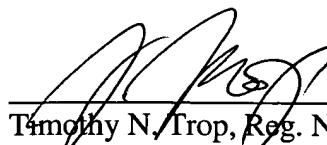
Sir:

In reply to the Office Communication mailed February 22, 2007, which requests a response to the double patenting rejection of the Office Action mailed September 15, 2006, a Terminal Disclaimer is attached, for the above-referenced application over U.S. Patent No. 6,741,978.

A check for \$130.00 is enclosed. If any additional fees are due, please deduct them from deposit account 20-1504 (ITL.0308C1US).

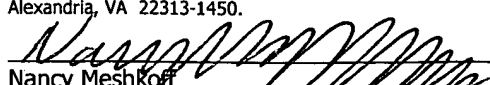
Respectfully submitted,

Date: March 8, 2007



Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
1616 South Voss Road, Suite 750
Houston, TX 77057-2631
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation

Date of Deposit: March 8, 2007
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Nancy Meshkov